Notice of References Cited	Application/Control No. 10/684,637	Applicant(s)/Patent Under Reexamination SIGNORE, NICHOLAS D.	
	Examiner Brandon W. Bowers	Art Unit 2825	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,446,676	08-1995	Huang et al.	703/19
*	B	US-5,880,967	03-1999	Jyu et al.	716/6
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	WO 200223409 A2	03-2002	World Intellect	EBERGEN et al.	G06F 17/50
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Berkelaar et al., "Computing the entire active area/power consumption versus delay tradeoff curve for gate sizing with piecewise linear simulator',' IEEE Transaction on Computer-Aided Design, vol. 15, pp. 1424-14
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.